

S/N 10/593674

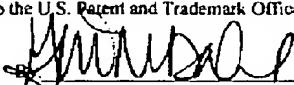
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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	TANIMURA ET AL.	Examiner:	LEE, KYUNG S
Serial No.:	10/593674	Group Art Unit:	2832
Filed:	September 21, 2006	Docket No.:	10921.0427USWO
Title:	CHIP RESISTOR AND MANUFACTURING METHOD THEREOF		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on April 27, 2009.



Name: Gina Dahl

Mail Stop: AMENDMENT  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

AMENDMENT

Dear Commissioner:

In response to the Office Action mailed November 25, 2008, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of this paper.

**Remarks/Arguments** begin on page 5 of this paper.